

Notice of Allowability	Application No.	Applicant(s)
	10/026,639	TATSUTA ET AL.
	Examiner Lourdes C. Cruz	Art Unit 2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to An Amendment filed 06/11/2003.
2. The allowed claim(s) is/are 1-21 and 27.
3. The drawings filed on _____ are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

5. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
 - (a) The translation of the foreign language provisional application has been received.
6. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application. **THIS THREE-MONTH PERIOD IS NOT EXTENDABLE**

7. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
8. CORRECTED DRAWINGS must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No. _____.
 - (b) including changes required by the proposed drawing correction filed 11 June 2003, which has been approved by the Examiner
 - (c) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No. _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet.

9. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1 <input type="checkbox"/> Notice of References Cited (PTO-892)	2 <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3 <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	4 <input type="checkbox"/> Interview Summary (PTO-413), Paper No. _____.
5 <input type="checkbox"/> Information Disclosure Statements (PTO-1449), Paper No. _____.	6 <input checked="" type="checkbox"/> Examiner's Amendment/Comment
7 <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit of Biological Material	8 <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance
	9 <input type="checkbox"/> Other

DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the title:

Please substitute the title for:

"Semiconductor-Chip Mounting Substrate Having at Least One Projection Thereon and a Pressure Holding Means"

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance: The present invention specifically describes a semiconductor device comprising a substrate having at least one integrally molded projection thereon, a first bump made of a conductive layer, a chip comprising a second bump; wherein the chip is mounted on said substrate such that said first and second bumps are in contact. Also, the application claims the above comprising a pressure holding means for providing pressure between the first and second bumps.

In independent claim 1, the specific contact pressure range is recited; in independent claims 15 and 16, the substrate is more specifically described to have a concave, and to describe the specific structural disposition of the pressure holding

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means and the pressure holding means itself; in independent claims 18 and 19, the specifics of claims 15 and 16 above are recited, and the claim more specifically describes the semiconductor element; in Claim 20, the specifics of claims 15 and 16 are recited in combination with specific structural features of the encapsulation.

The above, in combination with all the specific features recited in each independent claim make each independent claim allowable over the prior art. The prior art fails to either alone or in combination anticipate the present invention as recited in the independent claims. Hence the Notice of Allowability found herein.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

The prior art made of record considered pertinent to applicant's disclosure includes Akram et al. (US 5789278), which discloses (See cover figure) a semiconductor chip mounting substrate (16A) comprising: At least one projection 44 thereon, a first bump 30, and a semiconductor chip 18 having a terminal projecting as a second bump on its surface, wherein said semiconductor chip is mounted on said substrate such that said first bump contacts said second bump.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Elle Cruz whose telephone number is 703-306-5691. The examiner can normally be reached on M-F 10-6:30.

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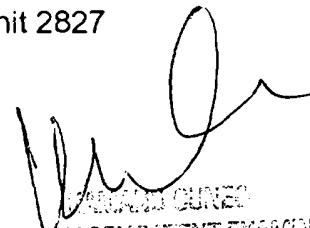
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammand Cuneo can be reached on 703-308-1233. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

Lourdes C. Cruz
Examiner
Art Unit 2827



Elle Cruz



KAMMAND CUNEO
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800